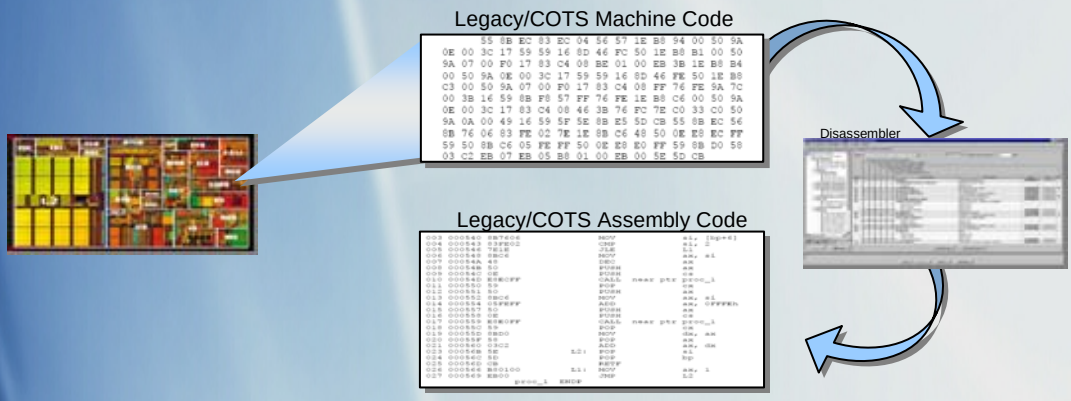
Anti-Tamper Application Counter Technology Binary Synthesis Translation

ATACT

Step 1

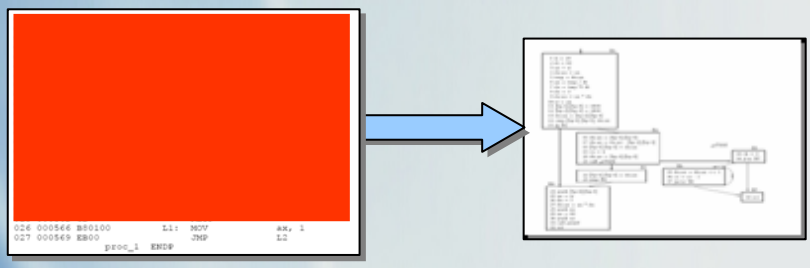
Identify/Extract Critical Program Information



Step 2

Critical Program Information Synthesis

CPI Selection is Analyzed

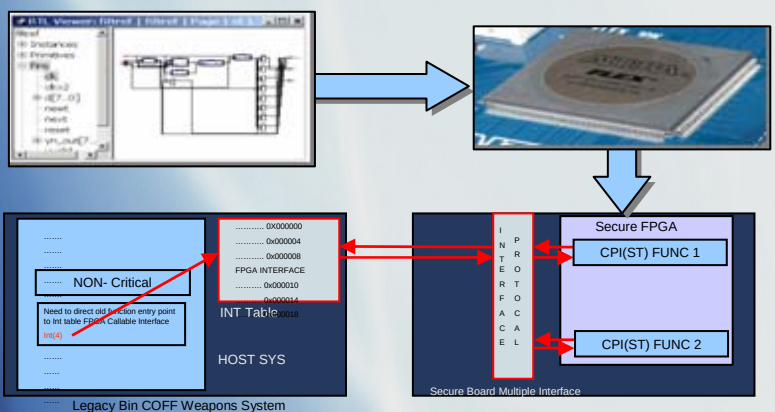


Build Internal Representation, Data Flow Analysis, Resolve Dependencies

Step 3

FPGA Instruction Optimization

Register Transfer Logic Viewer

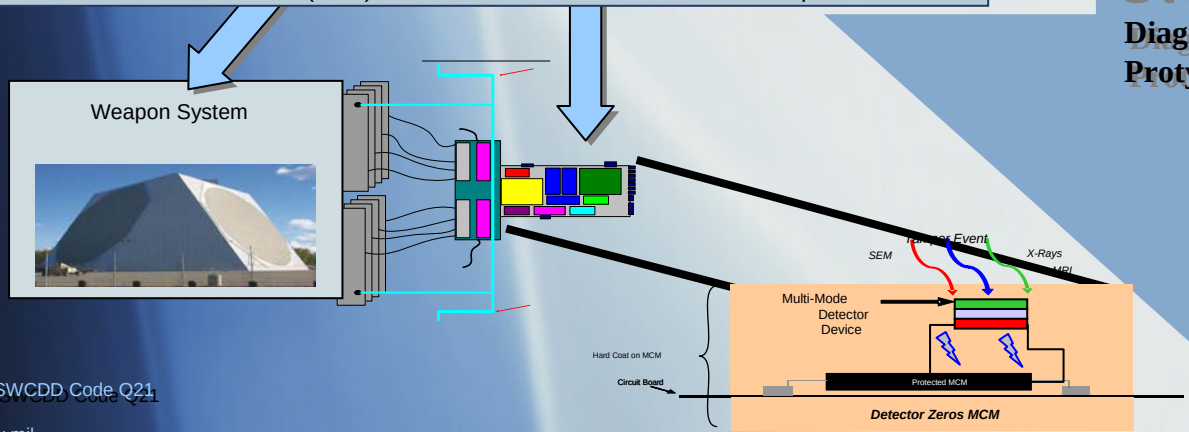


FPGA uses 128 bit Non-Volatile Key CPI program is encrypted

Step 4

Diagram of Protype Board

Remote Procedure Call (RPC) Client/Server Modules resident on both platforms



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Multi Tamper detection sensor X-ray, Mechanical/chemical ,Thermal, Ultrasonic wrapped around secure card.